# Department of Physics Osmania University Hyderabad



Scheme of instructions and syllabus (Choice Based Credit System)

**B.Sc. Electronics** 

With effect from: 2016-2017

# OSMANIA UNIVERSITY B.Sc. ELECTRONICS SYLLABUS SCHEME OF INSTRUCTIONS

# **UNDER CBCS (w.e.f 2016-2017 academic year onwards)**

Year	Semester	Title of the Paper[ Theory and Practical ]	Instructions Hrs/week	Number of Credits	Marks
1 <sup>st</sup> Year	I Sem	Paper – I : Circuit Analysis	4	4	100
		Practical – I : Circuit Analysis Lab	3	1	25
	II Sem	Paper – II: Electronic Devices	4	4	100
		Practical – II : Electronic Devices Lab	3	1	25
2 <sup>nd</sup> Year	III Sem	Paper – III: Analog Circuits	4	4	100
		Practical – III : Analog Circuits Lab	3	1	25
	IV Sem	Paper – IV : Linear Integrated circuits and basics of Communication	4	4	100
		Practical – IV : Linear Integrated Circuits and basics of communication Lab	3	1	25
3 <sup>rd</sup> Year	V Sem	Paper –V: Digital Electronics	3	3	75
		Practical – V : Digital Electronics Lab	3	1	25
		Paper – VI : Discipline Specific Elective – i. 8085 Microprocessor and applications ii. Electronic Instrumentation	3	3	75
		Practical – VI : i. 8085 Microprocessor and applications Lab ii. Electronic Instrumentation Lab	3	1	25
	VI Sem	Paper – VII : Digital Communication	3	3	75
		Practical – VII : Digital Communication Lab	3	1	25
		Paper – VIII : Discipline Specific Elective – II: i. 8051 Micro Controller and applications ii. Digital System Design using VHDL	3	3	75
		Practical – VIII : Elective-II : i. 8051 Micro Controller and applications Lab ii. Digital System Design using VHDL Lab	3	1	25

**Total Credits: 36** 

#### B.Sc. ELECTRONICS SYLLABUS B.Sc. I YEAR Semester – I

**DSC- Paper –I: Circuit Analysis** 

Total number of hours: 60 No of hours per week:4 Credits:4

#### UNIT - I

**AC Fundamentals :** The sine wave –average and RMS values – The J Operator –Polar and Rectangular forms of complex numbers – Phasor diagram-Complex impedance and admittance.

**Kirchhoff's Current and Voltage Laws:** Concept of Voltage and current sources-KVL and KCL-application to simple circuits (AC and DC) consisting of resistors and sources – Node voltage analysis and Mesh analysis.

#### **UNIT-II**

**Network Theorems (DC and AC):** Superposition Theorem ,Thevenin's Theorem, Norton's Theorem, Maximum power transfer Theorem, Reciprocity Theorem, Milliman's Theorem, Application to simple Networks.

#### **UNIT-III**

**RC** and **RL** Circuits: Transient Response of RL and RC Circuits with step input, Time constants. Frequency response of RC and RL circuits, Types of filters – Low pass filter and High pass filter-frequency response, passive differentiating circuit and passive integrating circuit.

#### **UNIT-IV**

**Resonance :**RLC Series and parallel resonance circuits –Resonant frequency –Q Factor- Bandwidth-Selectivity.

**Cathode Ray Oscilloscope:** Cathode Ray Tube (CRT) and its working, electron gun focusing, deflection sensitivity, florescent screen. Measurement of Time period, Frequency, Phase and amplitude.

#### **Text Books:**

- 1) Basic Electronics-Bernard Grob10th edition (TMH)
- 2) Circuit Analysis-P.Gnanasivam Pearson Education
- 3) Circuit and Networks-A. Sudhakar& S. Pallri (TMH)
- 4) Pulse, digital & switching waveforms-Milliman & Taub.
- 5) Networks, Lines and Fields-John Ryder (PHI)
- 6) Network theory-Smarajit Ghosh (PHI)

#### B.Sc. I Year, Semester – I: Electronics Practical

#### Paper – I : Circuit Analysis Lab

No. of hours per week: 3

- 1. Measurement of peak voltage, frequency using CRO.
- 2. Measurement of phase using CRO.
- 3. Thevenin's theorem and Norton's theorem verification.
- 4. Maximum power transfer theorem verification.
- 5. CR circuit Frequency response (Low pass and High pass)
- 6. CR and LR circuits Differentiation and integration tracing of waveforms.
- 7. LCR Series resonance circuit frequency response Determination of f<sub>o</sub>, Q and band width.
- 8. Simulation: i) verification of KVL and KCL.
  - ii) study of network theorems.
  - iii) study of frequency response (LR).

#### Note: Student has to perform minimum of Six experiments.

#### Reference Books:

- 1) Lab manual for Electronic Devices and Circuits 4<sup>th</sup> Edition. By David A Bell PHI
- 2) Basic Electronics A Text Lab Manual –Zbar, Malvino, Miller.



# B.Sc. ELECTRONICS SYLLABUS B.Sc. I YEAR

#### Semester - II

**DSC- Paper –II : Electronic Devices** 

Total number of hours: 60 No of hours per week: 4

**Credits:4** 

#### **UNIT-I**

**PN Junction:** Formation of PN junction, Depletion region, Junction capacitance, Diode equation (no derivation) Effect of temperature on reverse saturation current, V - I characteristics and simple applications of i) Junction diode, ii) Zener diode, iii) Tunnel diode and iv) Varactor diode.

#### **UNIT-II**

**Bipolar Junction Transistor( BJT) :** PNP and NPN transistors, current components in BJT, BJT static characteristics ( Input and Output ) , Early effect , CB , CC , CE configurations of transistor and bias conditions ( cut off, active, and saturation regions ), CE configuration as two port network, h- parameter model and its equivalent circuit. Determination of h- parameters from the characteristics, Load line analysis ( AC and DC ). Transistor Biasing – Fixed and self bias.

#### **UNIT-III**

**Field Effect Transistor** (**FET**): Construction and working of JFET, output and transfer characteristics of FET, Determination of FET parameters. Application of FET as Voltage variable resistor. Advantages of FET over BJT.MOSFET:: construction and working of enhancement and depletion modes, output and transfer characteristics Application of MOSFET as a switch.

**Uni Junction Transistor (UJT):** Construction and working of UJT and its Characteristics. Application of UJT as a relaxation oscillator.

#### **UNIT-IV**

**Silicon Controlled Rectifier (SCR):** Construction and working of SCR. Two transistor representation, Characteristics of SCR. Application of SCR for power control.

**Photo electronic Devices:** Construction and Characteristics of Light Dependent Resistor (LDR), Photo voltaic Cell, Photo diode, Photo transistor and Light Emitting Diode(LED).

#### **Books Recommended:**

- 1) Electronic Devices and circuits-Millman and Halkias,(TMH)
- 2) Principles of Electronics-V.K.Mehta & Rohit Mehta
- 3) Electronic Devices and Circuits-Allen Moltershed (PHI)
- 4) Basic Electronics and Linear Circuits-Bharghava U
- 5) Electronic Devices and Circuits-Y.N.Bapat
- 6) Electronic Devices and Circuits-Mithal.
- 7) Experiments in Electronics-S.V.Subramanyam.

#### **B.Sc. I Year, Semester – II : Electronics Practical**

#### Paper – II: Electronic Devices Lab

No. of hours per week: 3

- 1. To draw volt- ampere characteristics of Junction diode and determine the cut in voltage, forward and reverse resistances.
- 2. Zener diode V I Characteristics Determination of Zener breakdown voltage.
- 3. Voltage regulator( line and load ) using Zener diode.
- **4.** BJT input and output characteristics (CE configuration) and determination of 'h' parameters.
- **5.** FET Characteristics and determination of FET parameters.
- **6.** UJT characteristics determination of intrinsic standoff ratio.
- 7. UJT as relaxation oscillator.
- **8.** Characteristics of LDR/Photo diode/Photo transistor/Solar cell.

Note: Student has to perform minimum of Six experiments.

#### **Reference Books:**

1) Lab manual for Electronic Devices and Circuits – 4<sup>th</sup> Edition. By David A Bell - PHI



### B.Sc. ELECTRONICS SYLLABUS B.Sc. II YEAR

#### Semester - III

**DSC-Paper - III: Analog Circuits** 

Total number of hours: 60 No of hours per week: 4

**Credits:4** 

#### <u>UNIT – I</u>

**Rectifiers and filters:** Rectifiers—half wave, full wave and bridge rectifiers, Efficiency, Ripple factor, regulation, harmonic components in rectified output, **Filters** — choke input (inductor) filter, Shunt capacitor filter, L section and  $\pi$  section filters.

#### UNIT – II

**Regulated Power Supplies:** Block diagram of regulated power supply, Series and shunt transistor regulated power supplies, three terminal IC regulators (78XX and 79XX), Principle and working of switch mode power supply (SMPS). UPS –Principle and working.

#### <u>UNIT – III</u>

**Transistor amplifier:** Classification of amplifiers, Hybrid  $\pi$  model of a transistor, RC coupled amplifier – frequency response and analysis.

**Feedback in amplifiers:** Positive and negative feedback, Effect of negative feedback on gain, bandwidth, noise, input and output impedances. Emitter follower, Darlington pair and its advantages

#### <u>UNIT – IV</u>

**Oscillators:** Barkhausen criterion for sustained oscillations, RC oscillators- RC phase shift and Wien's bridge oscillators, LC oscillators- Hartley and Colpitt.

**Multi-vibrators:** Astable, Mono stable and Bi-stable multi-vibrators (Qualitative treatment using BJT's only)

#### **Recommended Books:**

- 1. Electronic Devices and Circuits-Millman and Halkias (TMH)
- 2. Basic Electronics and linear circuits Bhargava, Kulshreshta & Gupta TMH
- 3. A first course in Electronics-AA Khan and KK Dey-PHI
- 4. Electronic Devices and Circuit Theory-Robert L Boylestad& Louis Nashelsky
- 5. Pulse, Digital and Switching circuits by Milliman and Taub

#### **B.Sc. II YEAR, Semester – III: Electronics Practical**

#### **Paper - III: Analog Circuits Lab**

No. of hours per week: 3

- 1. Study of HWR, FWR and bridge rectifier, determination of ripple factor.
- 2. Series inductor, shunt capacitor, L-section and  $\pi$ -section filters; determination of ripple factor using Full wave Rectifier.
- 3. Study of voltage regulator using IC's 78XX & 79XX.
- 4. Colpitt oscillator determination of frequency.
- 5. RC Phase shift oscillator- determination of frequency
- **6.** Astable multi-vibrator determination of time period and duty cycle.

#### 7. Simulation experiments:

- i) Rectifiers
- ii) RC coupled amplifier
- iii) Wein bridge oscillator
- iv) Colpitt oscillator
- v) RC phase shift oscillator
- vi) Astable multi-vibrator

#### Note: Student has to perform minimum of Six experiments

- 1) Lab manual for Electronic Devices and Circuits 4<sup>th</sup> Edition. By David A Bell PHI
- 2) Basic Electronics A Text Lab Manual –Zbar, Malvino, Miller.

#### **B.Sc. ELECTRONICS SYLLABUS**

#### **B.Sc. II YEAR, Semester - IV**

#### DSC- Paper - IV: Linear Integrated Circuits and Basics of Communication

Total number of hours: 60 No of hours per week: 4

**Credits:4** 

#### <u>UNIT – I</u>

**Operational Amplifiers:** Emitter Coupled Differential amplifier, Block diagram of Opamp. Characteristics of Opamp, Opamp parameters-Input resistance, Output resistance, Common mode rejection ratio (CMMR), Slew rate, offset voltages, Input bias current, Basic Op-Amp circuits-Inverting Op-Amp, Virtual ground, Non-inverting Op-Amp, Frequency response of Op-Amp. Op Amp as: Summing amplifier, subtractor, Comparator, Voltage follower, Integrator, and Differentiator.

#### **UNIT-II**

**Applications of Op-Amps**: Logarithmic amplifier, Sine wave [Wien Bridge] generator and square wave [Astable] generator, Triangular wave generator, Mono stable multi-vibrator, Solving of simple second order differential equations. Basic Op-Amp series regulator and shunt regulator, IC 555 Timer [Block diagram and its working], IC 555 as mono stable and astable multi-vibrators.

#### <u>UNIT – III</u>

**Modulation:** Need for modulation-Types of modulation-Amplitude, Frequency and Phase modulation. **Amplitude modulation:** Analysis of Amplitude modulation, side bands, modulation index, AM modulator, Balanced modulator, Demodulation – diode detector.

#### <u>UNIT – IV</u>

**Frequency modulation:** Analysis of FM, Working of simple frequency modulator, - detection of FM waves – FM Discriminator. Advantages of frequency modulation. AM and FM Transmitters and radio receivers [block diagram approach]. Introduction to PAM, PPM, PWM, and PCM, Delta modulation.

#### **Reference Books:**

- 1. Op amps and linear Integrated Circuits Ramakant Gayakwad, PHI
- 2. Linear Integrated Circuits- D Roy Choudhury and Shail B Jain
- 3. Electronic Communication Systems-George Kennedy & Bernard Davis
- 4. Principles of Electronic Communication Systems-Louis E Freznel, TMH

#### **B.Sc. II YEAR**, Semester – IV: Electronics Practical

#### Paper - IV: Linear Integrated Circuits and Basics of Communication Lab

Total number of hours per week: 3

#### Practical: Using IC 741OpAmp and IC 555 Timer:

- 1. Op amp as inverting Amplifier- determination of gain (with AC and DC).
- 2. Op amp as non-inverting Amplifier- determination of gain( with AC and DC ).
- 3. OP Amp as Summing amplifier and comparator( Zero crossing detector)
- 4. Astable multi-vibrator determination of time period and duty cycle.
- 5. Mono stable multi-vibrator- determination of gate width.
- 6. Integrator/ Differentiator study of wave forms.
- 7. Astable multi-vibrator using IC 555
- 8. Mono stable multi-vibrator using IC 555.
- 9. AM modulator and detector
- 10. FM modulator and detector

#### Simulation of all the above experiments:

- 1. Inverting and Non-inverting amplifiers and comparator
- 2. Integrator/ Differentiator using op amp
- 3. Wein bridge oscillator
- 4. Astable multi-vibrator using Op Amp
- 5. Astable multi-vibrator using IC 555

#### Note: Student has to perform minimum of Six experiments

- 1) Lab manual for Electronic Devices and Circuits 4<sup>th</sup> Edition. By David A Bell PHI
- 2) Basic Electronics A Text Lab Manual –Zbar, Malvino, Miller.

#### **B.Sc. ELECTRONICS SYLLABUS**

#### **B.Sc. III YEAR, Semester - V**

**DSC-** Paper - V: Digital Electronics

Total number of hours: 45 No of hours per week: 3

**Credits:4** 

#### **UNIT-I**

**Number system and Logic gates:** Conversions of Binary, octal, Decimal & hexadecimal number systems, Binary addition and subtraction (1's and 2's complement methods).

**Logic gates-** OR, AND, NOT, XOR, NAND, NOR gates and their Truth tables — Design of basic gates using the Universal gates- NAND and NOR gates, Half adder, Full adder and parallel adder logic circuits. Logic families and their characteristics — TTL, CMOS and ECL logic circuits.

#### **UNIT-II**

**Boolean algebra and Combinational logic circuits:** Boolean algebra- Laws and identities, DeMorgan's Theorems. Simplification of Boolean expressions using Boolean identities- Reduction of Boolean expressions using Karnaugh Maps - Sum of Products (SOP) representation (up to four variables). Multiplexer, De-Multiplexer, Decoder (3 to 8) and Encoder (8 to 3).

#### **UNIT-III**

**Sequential logic circuits:** Flip-flops - SR, D, JK, T and Master-Slave JK; **Registers** - Shift Registers-SISO,SIPO, PISO and PIPO Registers, Universal shift register (IC 7496), **Shift register counters-** Ring counter, Johnson Counter.

#### **UNIT-IV**

#### **Counters and Semiconductor memories:**

4-bit Asynchronous ( Ripple ) counter, Modulo-N counter, synchronous counter. Up/down counters – ripple counter IC7493 - Decade counter IC7490 – working, truth tables and timing diagrams.

**Semiconductor memories** :: Organization and working of ROM, types of ROM's - PROM, EPROM, EEPROM, FLASH, RAM- static and dynamic.

#### **Books Recommended:**

- 1. Digital Principles and Applications Malvino& Leach TMH.
- 2. Digital Principles and Applications-Ronald J.Tocci-Pearson Education.
- 3. Text book of Electronics Bsc III year (vol.III)-Telugu Akademi
- 4. Digital Fundamentals F.Loyd& Jain Pearson Education.
- 5. Fundamentals of Digital Circuits Anand Kumar PHI
- 4. Digital Electronics Principles and Integrated circuits Maini Wiley India.
- 5. Digital Electronics Gothman

#### **B.Sc. ELECTRONICS SYLLABUS**

#### B.Sc. III YEAR, Semester – V Practical

#### Paper -V: Digital Electronics Lab

#### No. of hour per week:3

- 1. Verification of truth tables of AND, OR, NOT, NAND, NOR, EXOR Gates using IC 74XX series.
- 2. Construction of basic gates using NAND and NOR gates.
- 3. Construction of Half Adder using gates. Verification of truth table.
- 4. Construction of Full Adder using gates and verification of truth table.
- 5. Verification of truth tables of flip flops: RS, D, and JK using IC's.
- 6. Construction of binary counters 7493

#### **Simulation experiments:**

- 1. 4bit parallel adder using Full adders.
- 2. Decade counter using JK flip flops.
- 3. Up/Down counters using JK flip flops.
- 4. Up/down counter using 74193
- 5. Multiplexer/De-Multiplexer.
- 6. Encoder.

#### Note: Student has to perform minimum of Six experiments

- 1. Lab manual for Electronic Devices and Circuits 4<sup>th</sup> Edition. By David A Bell PHI
- 2. Basic Electronics A Text Lab Manual –Zbar, Malvino, Miller.

#### B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR, Semester - V

Paper – VI A (Elective)

#### DSE- 01:8085 Microprocessor and Applications

Total number of hours: 45 No of hours per week: 3

Credits :4

#### **UNIT-I**

**Introduction to 8085 Microprocessor & its architecture::** Introduction to Microcomputer, Intel 8085 Microprocessor – Architecture of 8085 microprocessor – CPU – Timing & Control Unit – Instruction cycle, Fetch Cycle, Execute cycle (Timing diagram), Machine cycle and clock states. Interrupts – Hardware and Software, Address space partitioning – Memory mapped I/O & I/O mapped I/O.

#### **UNIT-II**

**Instruction set of 8085 microprocessor:** Classification - Data transfer operations, Arithmetic operations, logical operations, Branch control operations and stack, I/O and Machine control operations. Stack and Subroutines, Addressing modes

#### **UNIT-III**

**Programming of 8085 microprocessor:** Assembly language programming, addition (8 and 16 bit), 8 bit - subtraction, multiplication and division. Finding the largest and smallest number in data array Program to arrange the given numbers in ascending and descending order. Counters and Time delays

#### **UNIT-IV**

**Interfacing of peripherals:** Types of programmable and non programmable interfacing peripherals-8212( I/O port)—programmable peripheral interface 8255.

D/A Converters( binary weighted, R-2R ladder network), A/D Converters( Dual slope , Successive approximation), Closed loop and open loop process control systems(concept only), Stepper motor control.

#### **Books Recommended:**

- 1) Microprocessor Architecture and Programming Ramesh S.Goanker Penram.
- 2) Fundamentals of Microprocessors and Micro controllers B.Ram, Dhanpat rai & sons.
- 3) Text book of Electronics B.SC III year (Vol.III)-Telugu Academy.
- 4) Introduction to Microprocessor Aditya P.Mathur TMH.
- 5) Microprocessor Lab Premier K.A. Krishnamurthy.

# **B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR , Semester – VI Practical**

#### Paper - VII: 8085 Microprocessor Lab

No. of hours per week:3

#### I. 8085 – Software Experiments:

- 1. Binary addition (8 bit and 16 bit )and subtraction (8 bit).
- 2. Decimal Addition (DAA).
- 3. Multiplication and Division (8 bit).
- 4. Picking of largest/Smallest number from the given data.
- 5. Arranging the given data in ascending/descending order.
- 6. Time Delay generation.

#### II. 8085 - Hardware Experiments:

- 1. R 2R ladder network (DAC) (4 bits).
- **2.** Interfacing a Stepper motor and rotating it clockwise/anticlockwise direction through a known angle.
- **3.** Interfacing a seven segment display.
- **4.** Interfacing ADC for temperature measurement.

Note: Student has to perform minimum of Six experiments

#### B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR, Semester - V

**Paper - VI –B (Elective)** 

#### **DSE: ELECTRONICS INSTRUMENTATION**

Total number of hours: 45 No of hours per week: 3

Credits :4

#### **Unit – I:** CHARACTERISTICS OF AN INSTRUMENT

Functional elements of a measurement system – Static characteristics – Accuracy, precision, bias, linearity, threshold, resolution, hysteresis, dead space, scale readability, span, static stiffness, input impedance, repeatability and reproducibility - Errors and calculation of errors in overall system – Dynamic characteristics – Zero, first and second order instruments - Responses for step, impulse, ramp and sinusoidal inputs.

#### **Unit –II:** TRANSDUCERS AND SENSORS

Definition of transducer and sensor – Classification of transducers – Pressure (strain gauge, piezoelectric transducer), displacement (potentiometric, LVDT), temperature (thermometer, thermistor, thermocouple) and photosensitive (Vacuum & gas filled phototubes, photomultiplier, photoconductive cell, photvoltaic cell) transducers.

#### **Unit –III: BRIDGE MEASUREMENTS**

Introduction - Wheatstone bridge - Kelvin bridge - Guarded Wheatstone bridge - AC bridges and their applications - Maxwell bridge - Hay bridge - Schering bridge - Wien bridge.

#### **Unit – IV:** TESTING INSTRUMENTS

Oscilloscopes – Block diagram – CRT Circuits – Vertical and horizontal deflection systems – Delay line, Multiple trace – Probes – Special oscilloscopes.

#### **Books for Study:**

- 1. C. S. Rangan, G. R. Sarma and V. S. V. Mani, 1999, Instrumentation Devices and Systems, *Tata McGraw-Hill, New Delhi*.
- 2. A. D. Helfrick and W. D. Copper, 1992, Modern Electronic Instrumentation and Measurement Techniques, *Prentice-Hall of India, New Delhi*.
- 3. A. K. Sawhney, A Course in Electrical and Electronic Measurement and Instrumentation, *Dhanpat Rai & Sons*.

#### **Books for Reference:**

- 1. E. O. Doebelin, 1983, Measurement Systems Application and Design, *International Edition, 3<sup>rd</sup> Ed., McGraw-Hill, NY.*
- 2. D. V. S. Moorthy, 1995, Transducer and Instrumentation, *Prentice-Hall of India.NewDelhi*.
- 3. J. W. Dalley, W. F. Riley and K. G. McConnel, 1993, Instrumentation for Measurements, *Wiley, NY*.
- 3. B. C. Nakre and K. K. Chaudry, Instrumentation Measurements and Analysis, *Tata McGraw-Hill, New Delhi*.
- 5. D. A. Skoog, Principles of Instrumental Analysis, 3<sup>rd</sup> Ed., Saunders College Publishing.

#### B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR, Semester – V

#### DSE - Paper - VI- B :ELECTRONICS INSTRUMENTATION LAB

No. of hours per week:3

#### I Analog Experiments:

- 1.Power control by SCR using UJT.
- 2. PLLas FM detector (using IC 565).
- 3. Active high pass filter.
- 4. Active low pass filter.
- 5. Calibration of Strain gauge.
- 6. LVDT.

#### **II** Analog Simulation Experiments (S/W):

- 1) Active filters using Op-Amp.
- 2) Frequency modulation and detection.
- 3) Amplitude modulation and detection.
- 4) Solution of differential equation using analog computation (using TUTSIM).

#### III Digital Experiments (H/W & S/W)

- 1. Construction of synchronous Up/Down Counter using IC 74192 and display using 7-segment display.
- 2. Implementation of Boolean functions using multiplexer.
- 3. Construction of shift registers using IC7495.
- 4. Construction of an 8-bit full adder using two 4-bit adders.
- 5. Given a four variable Boolean function design and simulate the circuit using gates.
- 6. Simulate a 4-bit binary/BCD decade counter.
- 7. Simulate a full adder circuit using Decoder/ Demodulator.
- 8. Simulate a 4-bit shift register.
- 9. Simulate a Johnson counter.

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#### B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR, Semester - VI

#### Paper - VII

#### **DSE: DIGITAL COMMUNICATION**

Total number of hours: 45 No of hours per week: 3 Credits:4

#### Unit -I:

Signals Analysis: Complex Fourier spectrum, Fourier transform, Properties of F.T, sampling theorem, random signals and noise, correlation and power spectrum.

<u>Unit- II:</u> Digital Communication Systems: A/D and D/A converter, Coded communication, AM, PWM, PPM, PCM, delta modulation, adaptive delta modulation, quantizationand noise consideration. Digital Transmission and Reception: Timing, base band systems, ASK, FSK, PSK, QAM.

<u>Unit - III:</u> Error detection and coding: Parity check, CRC, Hamming distance, Hamming codes, Cyclic codes, line synchronization codes, Manchester code, NRZ coding, Walsh codes.

<u>Unit -IV:</u>Case studies: Paging system, cellular telephone, global positioning satellite, Facsimile, Videotext.

#### **Reference Books:**

- 1. Analog and Digital Communication systems- M.S. Roden, 3rd Edition, Prentice Hall of India.
- 2. Modern Digital and Analog Communication Systems-B.P. Lathi.
- 3. Communication Techniques for digital and Analog signals M. Kanefsky, John Wiley and Son.
- 4. Telecommunication T.H. Brewster, McGraw Hill.
- 5. Principles of Digital communication, Das, Chatterjee and Mallick, Wiley Eastern Ltd.

# **B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR , Semester – VI Practical**

#### Paper - VII :DIGITAL COMMUNICATION Lab

No. of hours per week :3

#### I Experiments in Internetworking:

- 1) Testing of RJ-45 Cable (Straight/ Cross)
- 2) Introduction to LAN cable and Hub.
- 3) Verifying physical and logical address.
- 4) Sending data/ Data transfer from system to system.
- 5) Concept of HTTP.
- 6) File transfer FTP.
- 7) Introduction to server and client.
- 8) Introduction to network IP address.
- 9) Identification of NET ID using masks.
- 10) Mail transfer using SMTP.
- 11) Encryption (plain text to Hypertext).
- 12) Study of Router configuration.
- 13) Study of two networks between LAN and LAN/ MAN and MAN/ WAN and WAN.
- 14) Introduction to network devices.
- 15) Static Routing.
- 16) Basic RIP (observe RIP routers and understand the commands)
- 17) RIP V2.
- 18) OSPF (Open Shortest Path First)

#### **II Experiments in Data Communication**.

- 1) Study of serial communication.
- 2) Study of protocol in communications.
- 3) Study of Fiber optic communications.
- 4) Study of wireless communications.
- 5) Study of parallel communication.

Note: Minimum 10 experiments to be studied

#### **B.Sc. ELECTRONICS SYLLABUS** B.Sc. III YEAR, Semester - VI

Paper VIII – A (Elective)

DSE ; 8051 Microcontroller and Applications

**Total number of hours: 45** No of hours per week: 3

Credits:4

#### **UNIT-I**

The Microcontroller 8051: Overview and block diagram of 8051. Architecture and pin diagram of 8051. Data types and directives, Memory Organisation, register banks and Stack Pointer. PSW Register, other special function registers, I/O port organization. Interrupts and Timer/Counter modules.

#### **UNIT-II**

Instruction set of 8051 microcontroller: Classification- Data transfer, Arithmetic, logical, Single Bit, Jump, Loop and CALL instructions and their usage. Addressing modes - Immediate, Register, Direct, Indirect, Absolute addressing, Relative addressing, Indexed Addressing and accessing memory using various addressing modes.

#### **UNIT-III**

#### **Programming examples of microcontroller 8051:**

Addition, Subtraction, division, picking the smallest/largest number among a given set of numbers, arranging a given a set of numbers in ascending/descending order, Subroutines, I/O Programming, Bit manipulation. Accessing a specified port terminal and generating wave forms.

Timer/Counter Programming in 8051: Programming 8051 timers- basic registers of timers- Timer0, Timer1 registers. TMOD register, TCON register. Timer modes - Mode1, Mode2 programming. Counter mode programming. Program to generate time delay.

#### Unit - IV

**Serial communications:** Serial communication, Types, modes and protocols, Data transfer rates, serial communication program-SBUF and SCON registers, RS232 standards, Programming timer Interrupts,

**Applications of Micro controller:** Displaying information on a LCD, Interfacing a keyboard, Interfacing a temperature sensor, Interfacing of DAC 0808 to microcontroller, Interfacing of ADC 0804 to microcontroller, Seven segment LED.

#### **Books Recommended:**

- 1) The 8051 Microcontrollers and Embedded Systems Muhammad AliMazidi and Janice gillipsieMazidi – Pearson Education Asia, 4<sup>th</sup> Reprint, 2002.
- 2) Text book of Electonics Bsc III year (vol.III)-Telugu Akademi.
- 3) Fundamentals of Microprocessors and Microcontrollers B.Ram.
- 4) The 8051 Microcontroller architecture, programming and applications KennthJ. Ayala-Penram International Publishing, 1995.
- 5) Micro controllers-Theory and Applications-Ajay V.Deshmukh.
- 6) Micro-controller 8051, D. Karuna Sagar, Narosa

#### B.Sc. ELECTRONICS SYLLABUS B.Sc. III YEAR, Semester III

#### **Paper - VIII Practical (Elective)**

#### 8051 Microcontroller and applications Lab

No. of hours per week:3

#### **Experiments using 8051 microcontroller:**

- 1. Multiplication of two numbers using MUL command (later using counter method for repeated addition).
- 2. Division of two numbers using DIV command (later using counter method for repeated subtraction).
- 3. Pick out the largest/smallest number among a given set of numbers.
- 4. Arrange the given numbers in ascending/descending order.
- 5. Generate a specific time delay using timer/counter.
- 6. Interface ADC and a temperature sensor to measure temperature.
- 7. Interface DAC and generate a staircase wave form with a step duration and number of steps as variables.
- 8. Flash a LED connected at a specified out port terminal.
- 9. Interface stepper motor to rotate clock wise / anti clock wise through a given angle steps.

#### **Experiments with Keil Software:**

- 1. Write a program to pick out largest/smallest number among a given set of number.
- 2. Write a program to arrange a given set of numbers in ascending/descending order.
- 3. Write a program to generate a rectangular/square wave form at specified port.
- 4. Write a program to generate a time delay using timer registers.

Note: Student has to perform minimum of Six Experiments

#### **B.Sc. ELECTRONICS SYLLABUS**

#### B.Sc. III YEAR, Semester - VI Paper – VIII - B (Elective)

DSE: Digital System Design Using VHDL

Total number of hours: 45 No of hours per week: 3 Credits:4

#### <u>UNIT</u> – I

**Fundamental Concepts**: Modeling Digital Systems, Domains and Levels of Modeling, Modeling Languages, VHDL Modeling Concepts, Learning a New Language: Lexical Elements and Syntax.

**Scalar Data Types and Operations**: Constants and Variables, Scalar Types, Type Classification, Attributes of Scalar Types, Expressions and Operators.

**Sequential Statements**: If Statements, Case Statements, Null Statements, Loop Statements, Assertion and Report Statements.

#### UNIT – II

Composite Data Types and Operations: Arrays, Unconstrained Array Types, Array Operations and Referencing, Records.

**Basic Modeling Constructs**: Entity Declarations, Architecture Bodies, Behavioral Descriptions, Structural Descriptions, Design Processing.

**Subprograms:** Procedures, Procedure Parameters, Concurrent Procedure Call Statements, Functions, Overloading, Visibility of Declarations.

#### UNIT – III

**Packages and Use Clauses**: Package Declarations, Package Bodies, Use Clauses, The Predefined Package Standard.

**Resolved Signals:** Basic Resolved Signals, IEEE Std\_Logic\_1164 Resolved Subtypes, Resolved Signals and Ports, Resolved Signal Parameters.

#### <u>UNIT – IV</u>

**Generic Constants**: Parameterizing Behavior, Parameterizing Structure.

Case Study: A Pipelined Multiplier Accumulator: Algorithm Outline, A Behavioral Model, A Register-Transfer-Level Model.

#### **Recommended Books:**

- 1. The Designer's Guide to VHDL -By Peter J. Ashenden, 2<sup>nd</sup> Ed., 1<sup>st</sup>Indian Reprint, Harcourt India Pvt. Ltd., 2001.
- 2. VHDL Programming by Example By Douglas L. Perry., 4<sup>th</sup> Ed., TMH., 2002
- 3. Introductory VHDL: From Simulation to Synthesis –By Sudhakar Yalamanchili., Pearson Education Asia. 2001
- 4. A VHDL Primer By J.Bhasker., Pearson Education Asia, 11th Indian Reprint, 2004
- 5. Fundamentals of Digital Logic with VHDL Design By Stephen Brown & ZvonkoVranesic., TMH. 2002
- 6. Digital Systems Design using VHDL by Charles H.Roth Jr., PWS Pub.,1998
- 7. VHDL Analysis & Modeling of Digital Systems By Zainalabedin Navabi., 2<sup>nd</sup> Ed., MH., 1998

#### **B.Sc. ELECTRONICS SYLLABUS**

#### B.Sc. III YEAR, Semester – VI DSE: Paper- VIII - B Practical (Elective)

#### VHDL - LAB

Number of hour per week:3

# VHDL – Program entry, simulation and Implementation (CPLD/FPGA) using appropriate HDL Software for the following circuits.

- 1. All types of logic gates (Data flow ).
- 2. Half Adder (Data Flow, Structural and Schematic ).
- 3. Full Adder (Data Flow, structural and Schematic).
- 4. Half Subtractor(Data Flow, Structural and Schematic).
- 5. Full Subtractor(Data Flow, Structural and Schematic).
- 6. Two control input Mux. Using case.
- 7. Two control input Mux. Using conditional signal assignment.
- 8. Two control input Mux. Using selected signal assignment.
- 9. Two control input Demux. Using case.
- 10. BCD to seven segment decoder.
- 11. Modeling a RSFF with assertion, report and different levels of severity (Behavioral).
- 12. Modeling a BCD counter (Top level behavioral)
- 13. Writing a test bench for a Half adder.
- **14.** Writing a test bench for a Full adder.

Note: Student has to perform minimum of Six experiments

# B.Sc. Semester III

### Skill Enhancement Course (SEC)

(W.E.F the academic year 2016-2017) (CBCS)

#### RENEWABLE ENERGY AND ENERGY HARVESTING

(Credits: 02) Theory: 30 Lectures

The aim of this course is not just to impart theoretical knowledge to the students but to provide them with exposure and hands-on learning wherever possible

#### Fossil fuels and Alternate Sources of energy:

Fossil fuels and Nuclear Energy, their limitation, need of renewable energy, non-conventional energy sources. An overview of developments in Offshore Wind Energy, Tidal Energy, Wave energy systems, Ocean Thermal Energy conversion, solar energy, biomass, biochemical conversion, biogas generation, geothermal energy tidal energy, Hydroelectricity. (3 Lectures)

#### Solar energy:

Solar energy, its importance, storage of solar energy, solar pond, non-convective solar pond, applications of solar pond and solar energy, solar water heater, flat plate collector, solar distillation, solar cooker, solar green houses, solar cell, absorption air conditioning. Need and characteristics of photovoltaic (PV) systems, PV models and equivalent circuits, and sun tracking systems. (6 Lectures)

### Wind Energy harvesting:

Fundamentals of Wind energy, Wind Turbines and different electrical machines in wind turbines. Power electronic interfaces, and grid interconnection topologies. (3 Lectures)

### Ocean Energy:

Ocean Energy Potential against Wind and Solar, Wave Characteristics and Statistics, Wave Energy Devices, (3 Lectures)

Tide characteristics and Statistics, Tide Energy Technologies, Ocean Thermal Energy, Osmotic Power, Ocean Bio-mass. (2 Lectures)

# Geothermal Energy:

Geothermal Resources, Geothermal Technologies. (2 Lectures)

### Hydro Energy:

Hydropower resources, hydropower technologies, environmental impact of hydro power sources.

(2 Lectures)

# Piezoelectric Energy harvesting:

Introduction, Physics and characteristics of piezoelectric effect, materials and mathematical description of piezoelectricity, Piezoelectric parameters and modeling piezoelectric generators. Piezoelectric energy harvesting applications, Human power (4 Lectures)

Electromagnetic Energy Harvesting: Linear generators, physics mathematical models, recent applications (2 Lectures)

Carbon captured technologies, cell, batteries, power consumption (2 Lectures)

Environmental issues and Renewable sources of energy, sustainability. (1 Lecture)

# **Demonstrations and Experiments**

- 1. Demonstration of Training modules on Solar energy, wind energy, etc.
- 2. Conversion of vibration to voltage using piezoelectric materials
- 3. Conversion of thermal energy into voltage using thermoelectric modules.

# Reference Books:

- 1. Non-conventional energy sources G.D Rai Khanna Publishers, New Delhi
- 2. Solar energy M P Agarwal S Chand and Co. Ltd.
- 3. Solar energy Suhas P Sukhative Tata McGraw Hill Publishing Company Ltd
- 4. Godfrey Boyle, "Renewable Energy, Power for a sustainable future", 2004.
- 5. Oxford University Press, in association with The Open University.
- 6. Dr. P Jayakumar, Solar Energy: Resource Assesment Handbook, 2009
- 7. J.Balfour, M.Shaw and S. Jarosek, Photovoltaics, Lawrence J Goodrich (USA).

# B.Sc. Semester III Skill Enhancement Course (SEC) (W.E.F the academic year 2016-2017) (CBCS)

# WEATHER FORECASTING (Credits: 02)

Theory: 30 Lectures

The aim of this course is not just to impart theoretical knowledge to the students but to enable them to develop an awareness and understanding regarding the causes and effects of different weather phenomenon and basic forecasting techniques

Introduction to atmosphere:

Elementary idea of atmosphere: physical structure and composition; compositional layering of the atmosphere; variation of pressure and temperature with height; air temperature; requirements to measure air temperature; temperature sensors: types; atmospheric pressure: its measurement; cyclones and anticyclones: its characteristics. (9 Periods)

Measuring the weather:

Wind; forces acting to produce wind; wind speed direction: units, its direction; measuring wind speed and direction; humidity, clouds and rainfall, radiation: absorption, emission and scattering in atmosphere; radiation laws. (4 Periods)

Weather systems: Global wind systems; air masses and fronts: classifications; jet streams; local thunderstorms; tropical cyclones: classification; tornadoes; hurricanes. (3 Periods)

Climate and Climate Change: Climate: its classification; causes of climate change; global warming and its outcomes; air pollution; aerosols, ozone depletion, acid rain, environmental issues related to climate. (6 Periods)

Basics of weather forecasting: Weather forecasting: analysis and its historical background; need of measuring weather; types of weather forecasting; weather forecasting methods; criteria of choosing weather station; basics of choosing site and exposure; satellites observations in weather forecasting; weather maps; uncertainty and predictability; probability forecasts. (8 Periods)

# Demonstrations and Experiments:

- 1. Study of synoptic charts & weather reports, working principle of weather station.
- Processing and analysis of weather data:
  - (a) To calculate the sunniest time of the year.
  - (b) To study the variation of rainfall amount and intensity by wind direction.
  - (c) To observe the sunniest/driest day of the week.
  - (d) To examine the maximum and minimum temperature throughout the year.
  - (e) To evaluate the relative humidity of the day.
  - (f) To examine the rainfall amount month wise.

- 3. Exercises in chart reading: Plotting of constant pressure charts, surfaces charts, upper wind charts and its analysis.
- 4. Formats and elements in different types of weather forecasts/ warning (both aviation and non aviation)

# Reference books:

- 1. Aviation Meteorology, I.C. Joshi, 3rd edition 2014, Himalayan Books
- 2. The weather Observers Hand book, Stephen Burt, 2012, Cambridge University Press.
- 3. Meteorology, S.R. Ghadekar, 2001, Agromet Publishers, Nagpur.
- 4. Text Book of Agrometeorology, S.R. Ghadekar, 2005, Agromet Publishers, Nagpur.
- 5. Why the weather, Charls Franklin Brooks, 1924, Chpraman & Hall, London.
- 6. Atmosphere and Ocean, John G. Harvey, 1995, The Artemis Press.

# B.Sc. Physics and B.Sc. ElectronicsSyllabus IIYEAR , Semester - IV

**Skill Enhancement Course:** BASIC INSTRUMENTATION SKILLS

#### With effect from 2016-2017

Total number of hours: 30 Number of credits: 2

This course is to get exposure with various aspects of instruments and their usage through handson mode.

#### UNIT I

**Basic of Measurement**: Instruments accuracy, precision, sensitivity, resolution rangeetc. Errors in measurements and loading effects.Multimeter: Principles of measurement of dc voltage and dc current, ac voltage, ac current and resistance. Specifications of amultimeter and their significance. (3 Lectures)

**Electronic Voltmeter**: Advantage over conventional multimeter for voltage measurement with respect to input impedance and sensitivity. Principles of voltage, measurement (block diagram only). Specifications of an electronic Voltmeter/ Multimeter and their significance. AC millivoltmeter: Type of AC millivoltmeters. Block diagram ac millivoltmeter, specifications and their significance. (4 Lectures)

**Oscilloscope**: Block diagram of basic CRO. CRT, electrostatic focusing and acceleration (Explanation only – no mathematical treatment), brief discussion on screen phosphor, visual persistence. Time base operation, synchronization. Front panel controls. Specifications of CRO and their significance. (5 Lectures)

Use of CRO for the measurement of voltage (dc and ac), frequency and time period. Special features of dual trace, introduction to digital oscilloscope, probes. Digital storage Oscilloscope: principle of working. (3 Lectures)

#### **UNIT II**

**Signal and pulse Generators**: Block diagram, explanation and specifications of low frequency signal generator and pulse generator. Brief idea for testing, specifications. Distortion factor meter, wave analysis. (4 Lectures)

**Impedance Bridges**: Block diagram of bridge. working principles of basic (balancing type) RLC bridge. Specifications of RLC bridge. Block diagram and working principles of a Q-Meter. Digital LCR bridges. (4 Lectures)

**Digital Instruments**: Comparison of analog & digital instruments. Characteristics of a digital meter. Working principles of digital voltmeter. (3 Lectures)

**Digital Multimeter**: Block diagram and working of a digital multimeter. Working principle of time interval, frequency and period measurement using universal counter/frequency counter, time- base stability, accuracy and resolution. (4 Lectures)

#### The test of lab skills will be of the following test items:

- 1. Use of an oscilloscope.
- 2. Oscilloscope as a versatile measuring device.
- 3. Use of Digital multimeter/VTVM for measuring voltages
- 4. Winding a coil / transformer.
- 5. Trouble shooting a circuit
- 6. Balancing of bridges

#### **Reference Books:**

A text book in Electrical Technology - B L Theraja - S Chand and Co. Performance and design of AC machines - M G Say ELBS Edn. Digital Circuits and systems, Venugopal, 2011, Tata McGraw Hill. Logic circuit design, Shimon P. Vingron, 2012, Springer.